

STM32Trust

More Trust with STM32



A whole security ecosystem around STM32

STM32Trust offers a robust multi-level strategy to enhance security in new product designs utilizing our STM32 Microcontrollers & microprocessor augmented with STSAFE secure elements.

STM32Trust is the security framework combining our knowledge, ecosystem and security services.

The solution offers a complete toolset for code and execution protection, ensures IP protection, data secured, and validated credentials are used, and helps to get firmware authenticity and secure firmware update.

THE SECURITY FUNCTIONS

STM32Trust offer results for accurate analysis of customer cases, ending up in the coverage of the base 12 security functions required for their needs

- Secure Boot
- Secure Install/Update
- Secure Storage
- Isolation
- Abnormal situations handling
- Crypto Engine
- Audit/Log
- Identification / Authentication / Attestation
- Silicon Device Lifecycle
- Software IP Protection
- Secure Manufacturing
- Application Lifecycle

1- Secure boot

Ability to ensure the authenticity and integrity of an application that is inside a device

2- Secure Install/Update

Installation or update of firmware with initial checks of integrity and authenticity before programming and executing

3- Secure Storage

Ability to securely store secrets like data or keys

4- Isolation

Isolation between trusted and non-trusted parts of an application

5- Abnormal situations handling

Ability to detect abnormal situations (both hardware and software) and to take adapted decisions like secrets removals

6- Crypto Engine

Ability to process cryptographic algorithms, as recommended by a security assurance level

7- Audit/Log

Keep trace of security events in an unchangeable way

8- Identification / Authentication / Attestation

Unique identification of a device and/ or software, and ability to detect its authenticity, inside the device or externally

9- Silicon Device Lifecycle

Control states to securely protect silicon device assets through a constrained path

10- Software IP Protection

Ability to protect a section or the whole software against external or internal reading. Can be multi-tenant

11- Secure Manufacturing

Initial device provisioning in unsecured environment with overproduction control. Potential secured personalization

12- Application Lifecycle

Define unchangeable incremental states to securely protect application states and assets

Those 12 Security Functions are completely or partially covered by ST offers, with combinations of hardware, software tools and services

Security Function	STM32F4/F7/L1/WB/G0/G4/H7/L0/L4		STM32MP1		STM32L5 with TrustZone		+ STSAFE-A/TPM
	Silicon	Firmware	Silicon	Firmware	Silicon	Firmware	Silicon
Secure Boot	✓	✓ SBSFU*	✓	✓ TF-A*	✓	✓ TFM*_SBSFU*	✓
Secure Install/Update	✓	✓ SBSFU*	✓	✓ OPTEE*	✓	✓ TFM*_SBSFU*	✓
Secure Storage	✓ (L0/L4/H7/G0/G4)	✓ (WB) SBSFU KMS* (L4)	✓	✓ OPTEE*	✓	✓ TFM SPE*	✓
Isolation	✓		✓	✓ OPTEE*	✓	✓ TFM*	✓
Abnormal situations handling	✓		✓		✓		
Crypto Engine	✓	✓ Crypto Libraries*	✓	✓ OPTEE*	✓	✓ Crypto Libraries* TFM*	✓
Audit/Log					✓	✓ TFM*	
ID/Auth/Attestation	✓		✓		✓	✓ TFM Attestation*	✓
Silicon Device LifeCycle	✓		✓		✓		
Software IP Protection	✓		✓	✓ OPTEE*	✓	✓ TFM*	
Secure Manufacturing	✓ SFI* (H7/L4) with STM32HSM		✓ SSP* with STM32HSM		✓ SFI* with STM32HSM		✓
Application LifeCycle	✓		✓		✓		✓

*All those solutions are defined at www.st.com/stm32trust

Light blue: Firmware to be developed by user
Yellow: Reference firmware proposed by ST

CERTIFICATIONS

ST is fully committed at certifying its solutions by independent recognized authority.

To discover this complete offer, please visit www.st.com/stm32trust

Available now			
CERTIFICATIONS		EVALUATIONS	
 <p>ARM PSA</p> <ul style="list-style-type: none"> Level 1 <p>STM32L4 STM32L5</p> <ul style="list-style-type: none"> Level 2 <p>STM32L5 (TFM)</p> <ul style="list-style-type: none"> API Compliant <p>STM32L5 (TFM)</p>	 <p>COMMON CRITERIA</p> <ul style="list-style-type: none"> CC EAL5+ <p>STSAFE-A110 STSAFE-TPM</p>	 <p>SESIP</p> <ul style="list-style-type: none"> Level 1 <p>STM32L4 (SBSFU)</p> <ul style="list-style-type: none"> Level 3 <p>STM32L4 (SBSFU)</p>	 <p>PCI</p> <ul style="list-style-type: none"> Point of Sale application <p>STM32L4</p>

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