

SPC584Nx, SPC58ENx, SPC58NNx IO definition: signal description and input multiplexing tables

Overview

This document applies to SPC584Nx, SPC58ENx, SPC58NNx devices in accordance with [Table 1](#).

The IO definition is contained in a Microsoft Excel[®] workbook file attached to this document. Locate the paperclip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.

This file includes the following information:

- Package pinouts
- Pin descriptions
 - Power supply and reference voltage pins
 - System pins
 - LVDS pins
 - Functional pins and associated alternate functions

The table below lists all part numbers available in the SPC584Nx, SPC58ENx, SPC58NNx family:

Table 1. Device summary

Package	Part number			
	4 MB		6 MB	
	Single core	Dual core	Dual core	Triple core
eLQFP176	SPC584N80E7	SPC58EN80E7	SPC58EN84E7	SPC58NN84E7
FPBGA292	SPC584N80C3	SPC58EN80C3	SPC58EN84C3	SPC58NN84C3

Revision history

Table 2. Document revision history

Date	Revision	Changes
03-Apr-2018	1	Initial release based on device pinout Microsoft Excel® file v2.3 Updates compared to the pinout Microsoft Excel® file v2.2 attached to the device Datasheet Rev3: <ul style="list-style-type: none">– improved Description of Ethernet RX_DV signal– Disclaimer update

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