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## Self-test configuration for SPC58ECx device

### Introduction

This document is a guideline about how to configure the STCU run self-test in SPC58ECx devices in both offline and online mode. The self-test consists of Logic and Memory BISTs (L/MBIST). It is used to detect latent failures and is transparent for the application. The reader should have a clear understanding of the usage of self-test. See [Reference documents](#) for additional details.

## 1 Overview

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The SPC58ECx device implements only the MBIST. It can detect latent faults in the volatile memory of the device. SPC58ECx includes 57 memory cuts. The reader can see the complete list on the chapter 7 (Device configuration) of the Reference Manual.

## 2 Self-test configuration

Self test can run either in online or offline mode. To reach the best trade off in term of consumption and execution time, we recommend to divide the MBISTs in 12 splits. The MBISTs inside the same split run in parallel mode. These 12 splits run in sequential mode.

The complete list of the MBISTs and its split is contained in the Microsoft Excel® workbook file attached to this document, see "SPC58ECx\_split.xlsx".

Locate the paperclip symbol on the left side of the PDF window, and click it. Double-click on the Excel® file to open it.

### 2.1 DFC list for offline configuration

MBISTs can run in offline mode up 180 MHz as max frequency. The list of the DCF to be configured in order to start up the MBIST during the boot phase (offline mode) is contained in the Microsoft Excel® workbook file attached to this document, see "SPC58ECx\_DCF.xlsx".

Locate the paperclip symbol on the left side of the PDF window, and click it. Double-click on the Excel® file to open it.

MBISTs run in full Run Mode in order to reach the maximum diagnostic coverage.

They take around 45 ms.

### 2.2 Online mode configuration

In online mode the split list remains the same with some limitations due to life cycle. All MBIST can run in online mode only in ST production and Failure Analysis (FA). In the other life cycle, HSM MBIST and FLASH MBIST are not accessible. Also in this case the maximum frequency is 180 MHz, provided by the sys\_clock. In that case STCU registers can be configured with the "register value" column of the DCF list file.

### 3 Summary

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In SPC58ECx can run only MBIST, both in offline and online mode with the same setting:

- frequency setting
- MBIST Algo
- MBIST sequence (split list)

## Appendix A Others informations

### A.1 Reference documents

Table 1. Reference documents

Doc Name	ID	Title	Revision
RM0407	028117	SCP58xx Microcontroller Reference Manuals	5
AN4551	026636	SPC574K72xx self-test procedures	1

### A.2 Acronyms

Table 2. Acronyms

Acronym	Name
LC	Life Cycle
DCF	Device Configuration Format (DCF) Records
UTest	User Test flash block
FA	Failure Analysis

## Revision history

**Table 3. Document revision history**

Date	Version	Changes
23-Aug-2019	1	Initial release.

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